FORM PTO-1449/A and B (modified PTO/SB/08)

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet 1 of 3

		APPLICATION NO.: 10/680,265	ATTY. DOCKET NO.: T0461.70041US0
--	--	-----------------------------	----------------------------------

FILING DATE: October 7, 2003 CONFIRMATION NO.: 6382

APPLICANT: Stefan Marinca et al

GROUP ART UNIT: 2825 EXAMINER: Yelena Rossoshek

U.S. PATENT DOCUMENTS

Examiner's	Cite	U.S. Patent Docu		Name of Patentee or Applicant of Cited	Date of Publication or Issue	
Initials #	No.	Number	Kind Code	Document	of Cited Document MM-DD-YYYY	
		6,828,847		Marinca	12-07-2004	
		7,193,454	2000	Marinca	03-20-2007	
		7,012,416 NOV 2	7000 3	Marinca	03-14-2006	
		7,211,993	, All	Marinca	05-01-2007	
		7,372,244	E/	Marinca	05-13-2008	
		7,173,407	MICH	Marinca	02-06-2007	
-		2008-0074172-A1		Marinca	03-27-2008	
		6,255,807		Doorenbos et al	07-03-2001	
		5,229,711		Inoue	07-20-1993	
		7,112,948		Daly et al	09-26-2006	
		7,170,336		Hsu	01-30-2007	
		7,236,047		Tachibana et al	06-26-2007	
		7,301,321		Uang et al	11-27-2007	
		2005-0194957-A1		Brokaw	09-08-2005	
		2008-0224759-A1		Marinca	09-18-2008	
		2008-0265860-A1		Dempsey et al	10-30-2008	
		6,853,238		Dempsey et al	02-08-2005	
		6,891,358	, ,	Marinca	05-10-2005	
		6,885,178		Marinca	04-26-2005	
		7,088,085		Marinca	08-08-2006	
		5,646,518		Lakshmikumar et al	07-08-1997	
		6,489,835		Yu et al	12-03-2002	
		5,119,015		Watanabe	06-02-1992	
		6,225,796		Nguyen	05-01-2001	
		6,828,847		Marinca	12-07-2004	
		7,193,454		Marinca	03-20-2007	

APPLICATION NO.: 10/680,265 ATTY. DOCKET NO.: T0461.70041US00

FORM PTO-1449/A and B (modified PTO/SB/08)

FILING DATE: October 7, 2003 CONFIRMATION NO.: 6382

APPLICANT: Stefan Marinca et al

Sheet 2 of 3

GROUP ART UNIT: 2825

EXAMINER: Yelena Rossoshek

U.S. PATENT DOCUMENTS

Examiner's	Cite	U.S. Patent Doc	ument	Name of Patentee or Applicant of Cited	Date of Publication or of issue	
Initials No.		Number Kind Code		Document	of Cited Document MM-DD-YYYY	
		4,603,291		Nelson	07-29-1986	
,		4,808,908		Lewis et al	02-28-1989	
		4,939,442		Carvajal et al	07-03-1990	
		5,053,640		Yum	10-01-1991	
		5,325,045		Sundby	06-28-1994	
		5,352,973		Audy	10-04-1994	
		5,424,628		Nguyen	06-13-1995	
		5,512,817		Nagaraj	04-30-1996	
		5,933,045		Audy et al	08-03-1999	
		5,982,201		Brokaw et al	11-09-1999	
		6,157,245		Rincon-Mora	12-05-2000	
		6,218,822		MacQuigg	04-17-2001	
		6,329,868		Furman	12-11-2001	
		6,373,330		Holloway	04-16-2002	
		6,426,669		Friedman et al	07-30-2002	
-		6,614,209		Gregoire, Jr.	09-02-2003	
		4,399,398		Wittlinger	08-16-1983	
		6,690,228		Chen Jun et al	02-10-2004	
		2003/234638		Eshraghi Aria et al	12-25-2003	
200000000000000000000000000000000000000	***************************************	000000000000000000000000000000000000000			***************************************	
000000000000000000000000000000000000000	000000000000000000000000000000000000000					

FOREIGN PATENT DOCUMENTS

Examiner's	Cite	Fore	eign Patent Docum	nent	Name of Patentee or Applicant of Cited	Date of Publication of Translation	
Initials #	No.	Office/ Country	Number	Kind Code	Document Cited Document MM-DD-YYYY		(Y/N)
500000000000000000000000000000000000000	000000000000000000000000000000000000000	200000000000000000000000000000000000000	60000000000000000000000000000000000000	000000000000000000000000000000000000000		***************************************	000000000000000000000000000000000000000
		EP	0510530		SGS-Thomson Microelectronics	10-28-1992	
		EP	1359490 A3		AMI Semiconductor, Inc.	11-05-2003	
		EP	1359490 A2		AMI Semiconductor, Inc.	11-05-2003	
		JP	4-167010		Olympus Optical Co.	06-15-1992	N
***************************************		***************************************	***************************************	***************************************	***************************************		
000000000000000000000000000000000000000		***************************************	*******************************	000000000000000000000000000000000000000	000000000000000000000000000000000000000	5500 5555555555555555555555555555555555	***************************************

EODM DTO	-1449/A and B (m	odified	I PT()/SD/08)	APPLICATION NO.:	10/680,265	ATTY. DOCKET NO.: T0461.70041US00
	•		·	FILING DATE: Octo	ber 7, 2003	CONFIRMATION NO.: 6382
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				APPLICANT:	Stefan Marinca et al	
				GROUP ART UNIT:	2825	EXAMINER: Yelena Rossoshek
Sheet	3	of	3	GROOF ART UNIT.	2023	LAANINGER. 1 CICHA ROSSUSHER

OTHER ART — NON PATENT LITERATURE DOCUMENTS

Examiner's Initials #	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)
000000000000000000000000000000000000000	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		020000000000000000000000000000000000000
		PCT/EP2008/058685 International Search Report and written opinion, October 1, 2008.	
		PCT/EP2008/051161 International Search Report and written opinion, May 16, 2008.	
		Chen, Wai-Kai, "The circuits and filters handbook", 2nd ed, CRC Press, 2003.	
		Cressler, John D., "Silicon Heterostructure Handbook", CRC Press-Taylor & Francis Group, 2006; 4.4-427-438.	
		Banba et al, "A CMOS bandgap reference circuit with Sub-1-V operation", IEEE JSSC Vol.34, No.5, May 1999, pp 670-674.	
		Brokaw, A. Paul, "A simple three-terminal IC bandgap reference", IEEE Journal of Solid-State Circuits, Vol. SC-9, No. 6, Dec 1974, pp. 388-393.	
		Jones, D.A., and Martin, K., "Analog Integrated Circuit Design", John Wiley & Sons, USA, 1997 (ISBN 0-47L-L4448-7, pp. 353-363)	
		Malcovati et al, "Curvature-compensated BiCMOS bandgap with 1-V supply voltage", IEEE JSSC, Vol.36, No.7, July 2001.	
		Sudha et al, "A low noise sub-bandgap voltage reference", IEEE, Proceedings of the 40th Midwest Symposium on Circuits and Systems, 1997. Volume 1, 3-6 August 1997, pp 193-196.	
		Widlar, Robert J., "New developments in IC voltage regulators", IEEE Journal of Solid-State Circuits, Vol. SC-6, No. 1, Feb 1971, pp. 2-7.	
		Gray, Paul R., et al, <u>ANALYSIS AND DESIGN OF ANALOG INTEGRATED CIRCUITS</u> , Chapter 4, 4th ed., John Wiley & Sons, Inc., 2001, pp. 253-327.	
		PCT/EP2005/052737 International Search Report, 9/23/2005.	

EXAMINER:	DATE CONSIDERED:
/Helen Rossoshek/	01/31/2009

[NOTE – No copies of U.S. patents, published U.S. patent applications, or pending, unpublished patent applications stored in the USPTO's Image File Wrapper (IFW) system, are included. See 37 CFR §1.98 and 12870G163. Copies of all other patent(s), publication(s), unpublished, pending U.S. patent applications, or other information listed are provided as required by 37 CFR §1.98 unless 1) such copies were provided in an IDS in an earlier application that complies with 37 CFR §1.98, and 2) the earlier application is relied upon for an earlier filing date under 35 U.S.C. §120.]

[#] EXAMINER: Initial if reference considered, whether or notcitation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

^{*}a copy of this reference is not provided as it was previously cited by or submitted to the office in a prior application, Serial No. ___, filed ___, and relied upon for an earlier filing date under 35 U.S.C. 120 (continuation, continuation-in-part, and divisional applications).